



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date

February 20, 2004 Ayesha J. Shaikh

Ayesha J. Shaikh

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : James W. Meyer and Cory Kanski	Attorney Docket No.: 501293.01 (30264/US)
Serial No. : 10/690,810	Group Art Unit : 2186
Filed : October 20, 2003	Examiner : Not Yet Assigned
Title : ARBITRATION SYSTEM AND METHOD FOR MEMORY RESPONSES IN A HUB-BASED MEMORY SYSTEM	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. A copy of the cited non-patent literature reference, as required under 37 C.F.R. § 1.98(a)(2), is enclosed. Copies of the cited U.S. patents and U.S. patent application publications will not be submitted herewith in accordance with the waiver by the Office of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for U.S. national patent applications filed after June 30, 2003. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

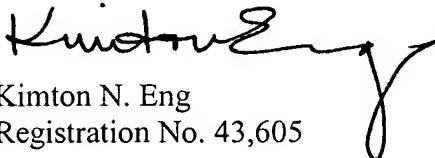
Applicants wish to make known to the Patent and Trademark Office that an application, Serial No. 10/695,383, describing and claiming subject matter that is similar to the subject matter of this application, was filed on October 27, 2003. This related application, as well as any prior art cited therein, may be material to the examination of this application. A copy of this related application as filed is submitted herewith in accordance with 37 C.F.R. § 1.98(d).

Applicants also wish to make known to the Patent and Trademark Office that an application, Serial No. 10/460,588, describing and claiming subject matter that is similar to the subject matter of this application, was filed on June 11, 2003. This related application, as well as any prior art cited therein, may be material to the examination of this application. A copy of this related application as filed is submitted herewith in accordance with 37 C.F.R. § 1.98(d).

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



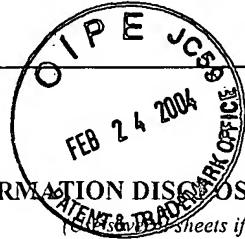
Kimton N. Eng
Registration No. 43,605

KNE:ajs

Enclosures:

- Postcard
- Form PTO-1449
- Cited Reference (1)
- Copies of Related Applications (2)

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FORM PTO-1449
(REV.7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE STATEMENT
(Continuation sheets if necessary)ATTY. DOCKET NO.
501293.01 (30264/US)APPLICATION NO.
10/690,810

APPLICANT(S)

James W. Meyer and Cory Kanski

FILING DATE

October 20, 2003

GROUP ART UNIT
2186

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,818,844	10/06/98	Singh et al.	370	463	
	AB	6,067,262	05/23/00	Irrinki et al.	365	201	
	AC	6,246,618 B1	06/12/01	Yamamoto et al.	365	200	
	AD	6,272,609 B1	08/07/01	Jeddeloh	711	169	
	AE	6,477,592 B1	11/05/02	Chen et al.	710	52	
	AF	6,523,092 B1	02/18/03	Fanning	711	134	
	AG	6,523,093 B1	02/18/03	Bogin et al.	711	137	
	AH	6,590,816 B2	07/08/03	Perner	365	200	
	AI	6,622,227 B2	09/16/03	Zumkehr et al.	711	167	
	AJ	6,631,440 B2	10/07/03	Jenne et al.	711	105	
	AK	2002/0144064 A1	10/03/02	Fanning	711	144	
	AL	2003/0005223 A1	01/02/03	Coulson et al.	711	118	
	AM	2003/0043426 A1	03/06/03	Baker et al.	359	109	
	AN	2003/0229770 A1	12/11/03	Jeddeloh	711	213	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AO							
	AP							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AQ	Intel, "Intel 840 Chipset: 82840 Memory Controller Hub (MCH)", Datasheet, October 1999, pp. 1-178.
AR	

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).